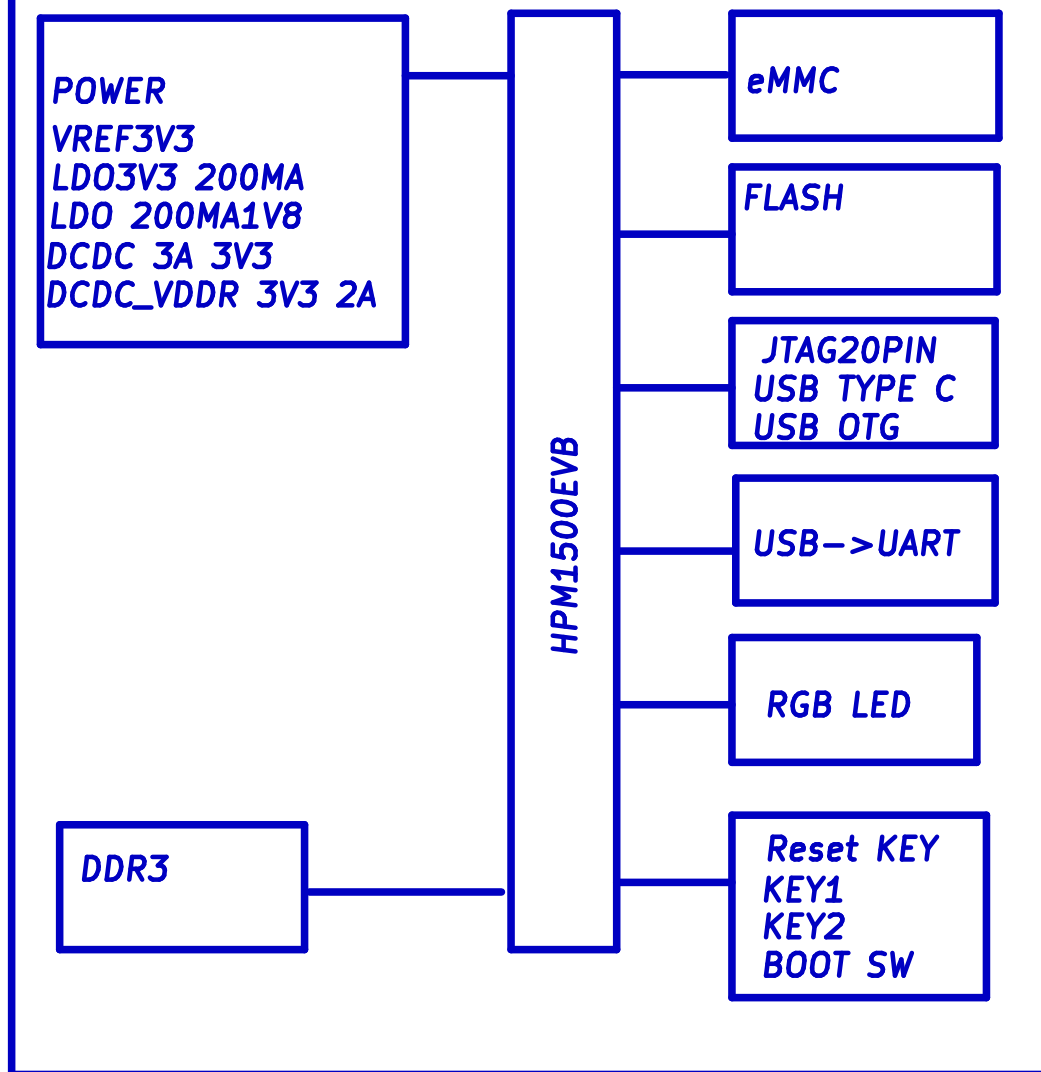


System block diagram



修订历史

内容	时间
HPM1500_DDR3_CORE_RevA JTAG 调试接口 (20pin) MCU+DDR Power by USB port eMMC as boot XPI as boot Boot code LED as indicator Button UART 300pin connector(100pin * 3)	20230312

System block diagram



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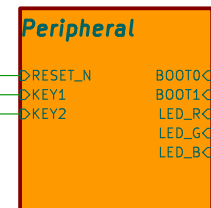
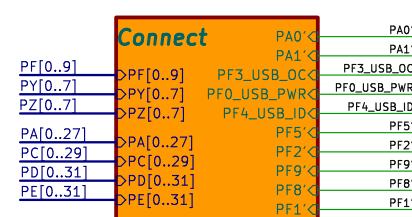
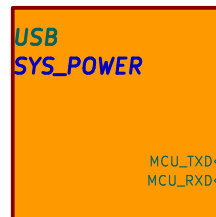
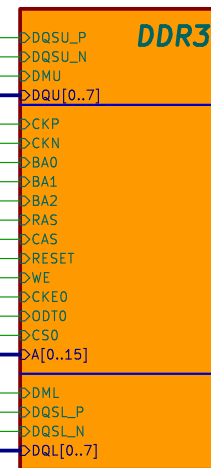
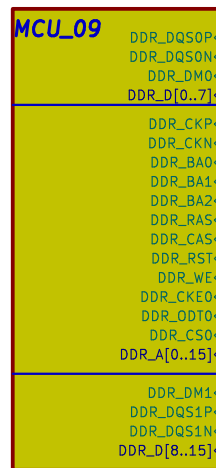
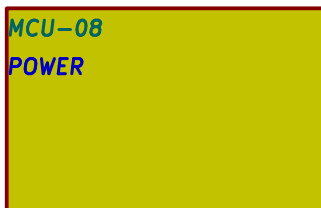
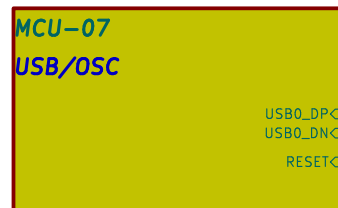
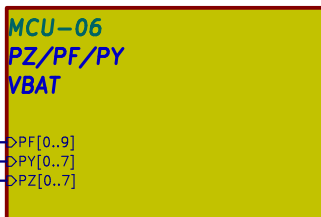
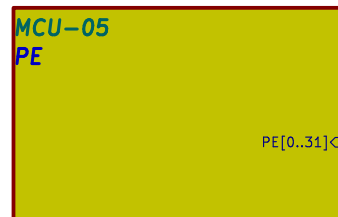
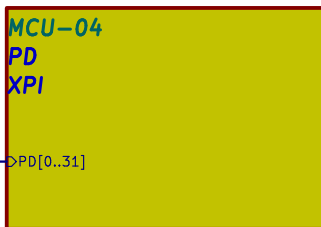
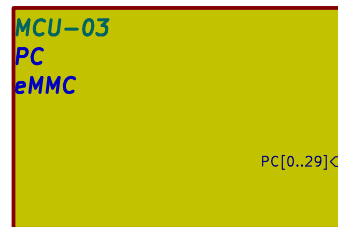
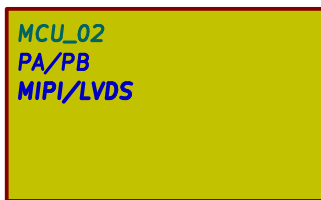
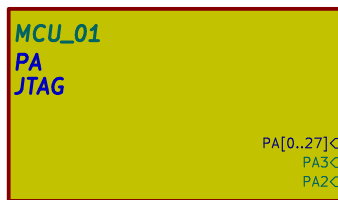
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01-Main



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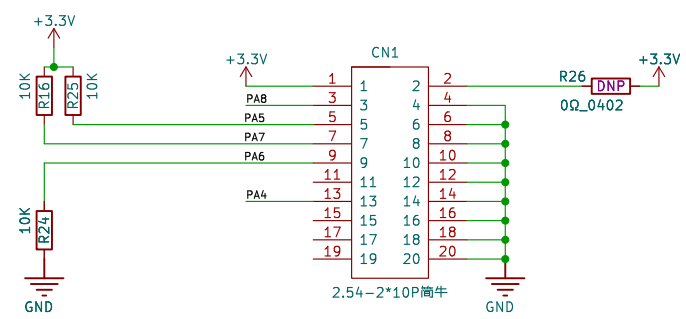
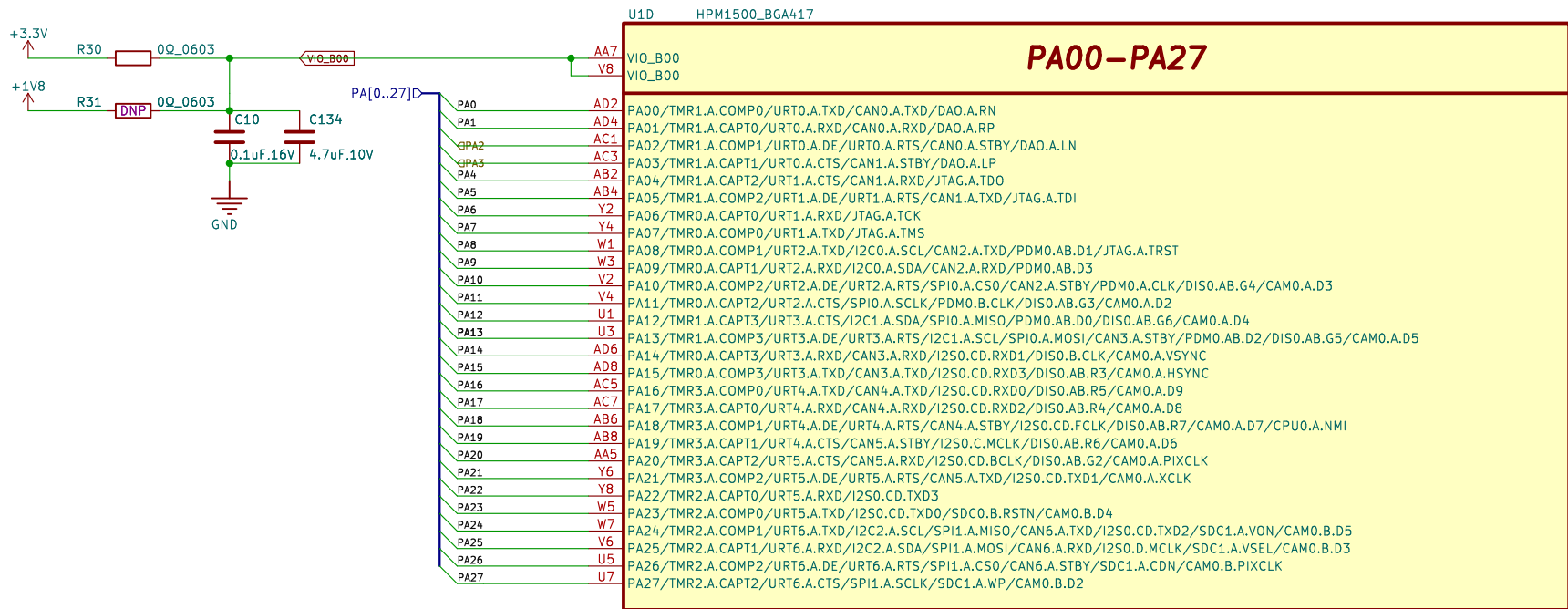
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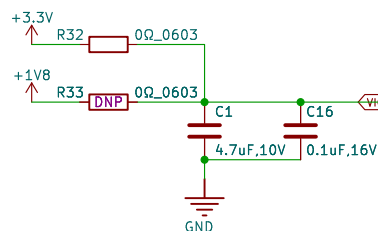
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02_MCU_01



MIPI/LVDS



引脚注意

PA28	M6
PA29	L5
MIPI0.D0_N	N1
MIPI0.D0_P	N3
MIPI0.CK_N	P2
MIPI0.CK_P	P4
MIPI0.D1_N	T2
MIPI0.D1_P	T4
MIPI2.D0_N	F2
MIPI2.D0_P	F4
MIPI2.D1_N	G1
MIPI2.D1_P	G3
MIPI2.CK_N	H2
MIPI2.CK_P	H4
MIPI2.D2_N	K2
MIPI2.D2_P	K4
MIPI2.D3_N	L1
MIPI2.D3_P	L3
PB14	M2
PB15	M4
MIPI1.D0_N	N5
MIPI1.D0_P	N7
MIPI1.CK_N	P6
MIPI1.CK_P	P8
MIPI1.D1_P	T8
MIPI1.D1_N	T6
MIPI3.D0_N	F6
MIPI3.D1_P	G7
MIPI3.D0_P	F8
MIPI3.D1_N	G5
MIPI3.CK_N	H6
MIPI3.CK_P	H8
MIPI3.D2_N	J5
MIPI3.D2_P	K6
MIPI3.D3_N	L7
MIPI3.D3_P	K8

U1E HPM1500_BGA417

PA28-PB31

VIO_B01	J7	PA28/TMR3.A.CAPT3/URT7.A.CTS/I2C3.A.SDA/DISO.AB.R0/MIPI0.A.REXT
VIO_B01	M8	PA29/TMR3.A.COMP3/URT7.A.DE/URT7.A.RTS/I2C3.A.SCL/CAN7.A.STBY/CAM0.B.XCLK/MIPI1.A.REXT
VIO_B01	R5	PA30/TMR2.A.CAPT3/URT7.A.RXD/CAN7.A.RXD/DISO.AB.R2/MIPI0.A.D0N/LVDS0.A.D0N
VIO_B01	R7	PA31/TMR2.A.COMP3/URT7.A.TXD/CAN7.A.TXD/DISO.AB.R1/MIPI0.A.D0P/LVDS0.A.D0P
		PB00/TMR5.A.COMP0/URT0.B.TXD/CAN0.B.TXD/DISO.AB.G1/MIPI0.A.CKN/LVDS0.A.CKN
		PB01/TMR5.A.CAPT0/URT0.B.RXD/CAN0.B.RXD/DISO.AB.G0/MIPI0.A.CKP/LVDS0.A.CKP
		PB02/TMR5.A.COMP1/URT0.B.DE/URT0.B.RTS/CAN0.B.STBY/DISO.AB.B1/MIPI0.A.D1N/LVDS0.A.D1N
		PB03/TMR5.A.CAPT1/URT0.B.CTS/SPI3.A.CS3/CAN1.B.STBY/DISO.AB.B0/MIPI0.A.D1P/LVDS0.A.D1P
		PB04/TMR5.A.CAPT2/URT1.B.CTS/SPI2.A.CS0/CAN1.B.RXD/DISO.AB.B2/MIPI2.A.D0N/LVDS2.A.D0N
		PB05/TMR5.A.COMP2/URT1.B.DE/URT1.B.RTS/SPI2.A.SCLK/CAN1.B.TXD/DISO.AB.G7/MIPI2.A.D0P/LVDS2.A.D0P
		PB06/TMR4.A.CAPT0/URT1.B.RXD/SPI2.A.MISO/DISO.AB.B3/MIPI2.A.D1N/LVDS2.A.D1N
		PB07/TMR4.A.COMP0/URT1.B.TXD/SPI2.A.MOSI/DISO.AB.B4/MIPI2.A.D1P/LVDS2.A.D1P
		PB08/TMR4.A.COMP1/URT2.B.TXD/I2C0.B.SCL/SPI3.A.CS2/CAN2.B.TXD/DISO.AB.B6/MIPI2.A.CKN/LVDS2.A.CKN
		PB09/TMR4.A.CAPT1/URT2.B.RXD/I2C0.B.SDA/SPI3.A.CS1/CAN2.B.RXD/DISO.AB.B5/MIPI2.A.CKP/LVDS2.A.CKP
		PB10/TMR4.A.COMP2/URT2.B.DE/URT2.B.RTS/SPI3.A.CS0/CAN2.B.STBY/DISO.AB.EN/MIPI2.A.D2N/LVDS2.A.D2N
		PB11/TMR4.A.CAPT2/URT2.B.CTS/SPI3.A.SCLK/DISO.AB.B7/MIPI2.A.D2P/LVDS2.A.D2P
		PB12/TMR5.A.CAPT3/URT3.B.CTS/I2C1.B.SDA/SPI3.A.MISO/DISO.AB.HSYNC/MIPI2.A.D3N/LVDS2.A.D3N
		PB13/TMR5.A.COMP3/URT3.B.DE/URT3.B.RTS/I2C1.B.SCL/SPI3.A.MOSI/CAN3.B.STBY/DISO.A.CLK/MIPI2.A.D3P/LVDS2.A.D3P
		PB14/TMR4.A.CAPT3/URT3.B.RXD/SPI3.A.DAT2/CAN3.B.RXD/DISO.AB.VSYNC/SYSTL.B.CLK_OBS1/MIPI2.A.REXT
		PB15/TMR4.A.COMP3/URT3.B.TXD/SPI3.A.DAT3/CAN3.B.TXD/SDC0.B.DS/SYSTL.B.CLK_OBS3/MIPI3.A.REXT
		PB16/TMR7.A.COMP0/URT4.B.TXD/CAN4.B.TXD/CAM0.B.D6/MIPI1.A.D0N/LVDS1.A.D0N
		PB17/TMR7.A.CAPT0/URT4.B.RXD/CAN4.B.RXD/CAM0.B.D7/MIPI1.A.D0P/LVDS1.A.D0P
		PB18/TMR7.A.COMP1/URT4.B.DE/URT4.B.RTS/CAN4.B.STBY/I2S1.AB.TXD1/CAM0.B.D9/MIPI1.A.CKN/LVDS1.A.CKN
		PB19/TMR7.A.CAPT1/URT4.B.CTS/SPI0.B.CS3/CAN5.B.STBY/I2S1.B.MCLK/CAM0.B.D8/MIPI1.A.CKP/LVDS1.A.CKP
		PB20/TMR7.A.CAPT2/URT5.B.CTS/SPI1.B.CS0/CAN5.B.RXD/I2S1.AB.TXD2/CAM0.B.HSYNC/MIPI1.A.D1P/LVDS1.A.D1P
		PB21/TMR7.A.COMP2/URT5.B.DE/URT5.B.RTS/SPI1.B.SCLK/CAN5.B.TXD/I2S1.AB.TXD0/CAM0.B.VSYNC/MIPI1.A.D1N/LVDS1.A.D1N
		PB22/TMR6.A.CAPT0/URT5.B.RXD/SPI1.B.MISO/I2S1.AB.TXD3/SDC0.B.DAT6/MIPI3.A.D0N/LVDS3.A.D0N
		PB23/TMR6.A.COMP0/URT5.B.TXD/SPI1.B.MOSI/I2S1.AB.FCLK/SDC0.B.DAT5/MIPI3.A.D1P/LVDS3.A.D1P
		PB24/TMR6.A.COMP1/URT6.B.TXD/I2C2.B.SCL/SPI0.B.CS2/CAN6.B.TXD/I2S1.A.MCLK/SDC0.B.DAT7/MIPI3.A.D0P/LVDS3.A.D0P
		PB25/TMR6.A.CAPT1/URT6.B.RXD/I2C2.B.SDA/SPI0.B.CS1/CAN6.B.RXD/I2S1.AB.BCLK/SDC0.B.DAT4/MIPI3.A.D1N/LVDS3.A.D1N
		PB26/TMR6.A.COMP2/URT6.B.DE/URT6.B.RTS/SPI0.B.CS0/CAN6.B.STBY/I2S1.AB.RXD1/SDC0.BC.DAT3/MIPI3.A.CKN/LVDS3.A.CKN
		PB27/TMR6.A.CAPT2/URT6.B.CTS/SPI0.B.SCLK/I2S1.AB.RXD3/SDC0.BC.DAT0/MIPI3.A.CKP/LVDS3.A.CKP
		PB28/TMR7.A.CAPT3/URT7.B.CTS/I2C3.B.SDA/SPI0.B.MISO/I2S1.AB.RXD0/SDC0.BC.DAT1/CPU0.B.NMI/MIPI3.A.D2N/LVDS3.A.D2N
		PB29/TMR7.A.COMP3/URT7.B.DE/URT7.B.RTS/I2C3.B.SCL/SPI0.B.MOSI/CAN7.B.STBY/I2S1.AB.RXD2/SDC0.BC.DAT2/MIPI3.A.D2P/LVDS3.A.D2P
		PB30/TMR6.A.CAPT3/URT7.B.RXD/SPI0.B.DAT2/CAN7.B.RXD/SDC0.BC.CLK/MIPI3.A.D3N/LVDS3.A.D3N
		PB31/TMR6.A.COMP3/URT7.B.TXD/SPI0.B.DAT3/CAN7.B.TXD/SDC0.BC.CMD/MIPI3.A.D3P/LVDS3.A.D3P

03_MCU_02



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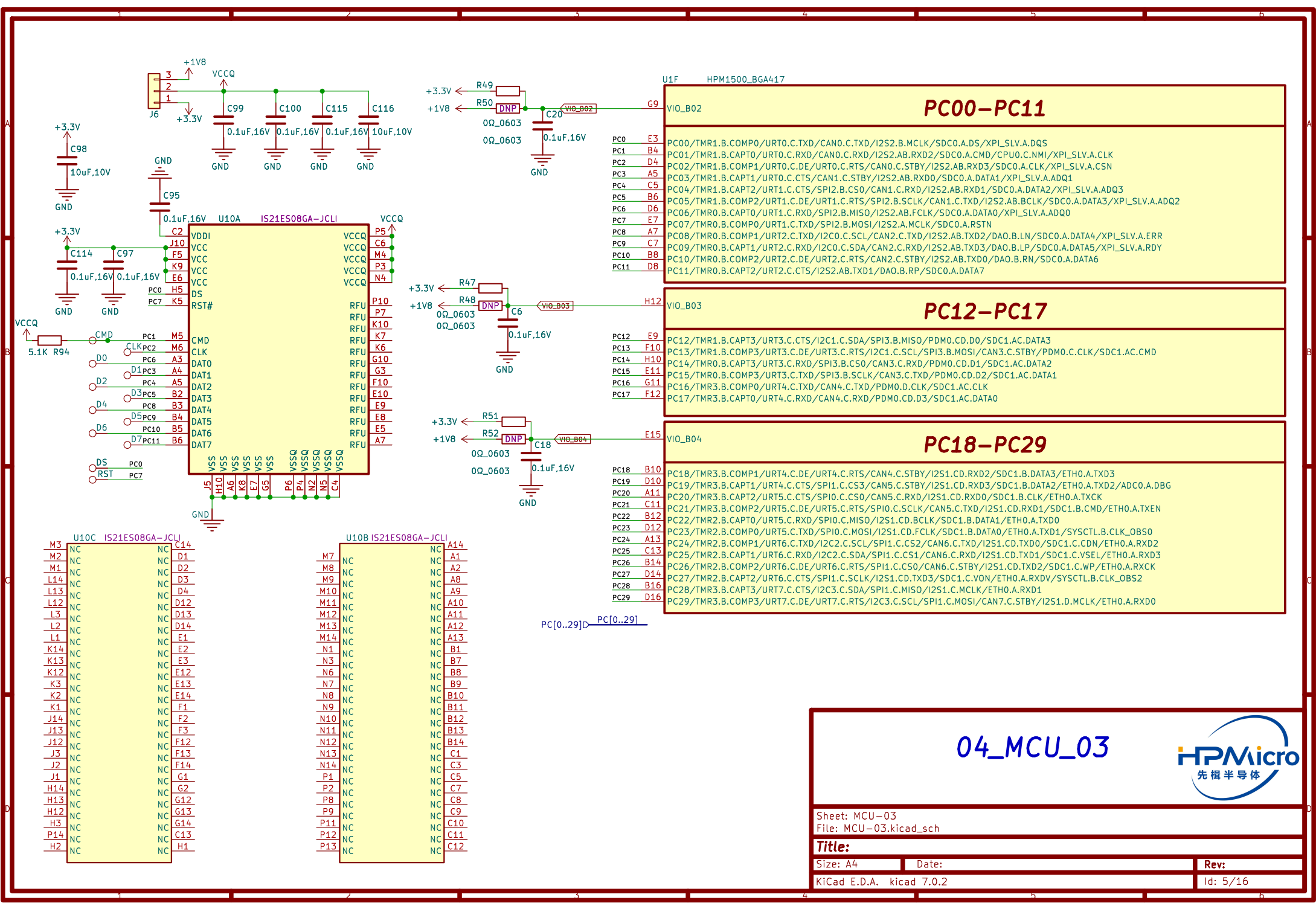
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PC00-PC11

PC0	E3	PC00/TMR1.B.COMP0/URT0.C.TXD/CAN0.C.TXD/I2S2.B.MCLK/SDC0.A.DS/XPL_SLV.A.DQS
PC1	B4	PC01/TMR1.B.CAPT0/URT0.C.RXD/CAN0.C.RXD/I2S2.AB.RXD2/SDC0.A.CMD/CPU0.C.NMI/XPL_SLV.A.CLK
PC2	D4	PC02/TMR1.B.COMP1/URT0.C.DE/URT0.C.RTS/CAN0.C.STBY/I2S2.AB.RXD3/SDC0.A.CLK/XPL_SLV.A.CSN
PC3	A5	PC03/TMR1.B.CAPT1/URT0.C.CTS/CAN1.C.STBY/I2S2.AB.RXD0/SDC0.A.DATA1/XPL_SLV.A.ADQ1
PC4	C5	PC04/TMR1.B.CAPT2/URT1.C.CTS/SPI2.B.CS0/CAN1.C.RXD/I2S2.AB.RXD1/SDC0.A.DATA2/XPL_SLV.A.ADQ3
PC5	B6	PC05/TMR1.B.COMP2/URT1.C.DE/URT1.C.RTS/SPI2.B.SCLK/CAN1.C.TXD/I2S2.AB.BCLK/SDC0.A.DATA3/XPL_SLV.A.ADQ2
PC6	D6	PC06/TMR0.B.CAPT0/URT1.C.RXD/SPI2.B.MISO/I2S2.AB.FCLK/SDC0.A.DATA0/XPL_SLV.A.ADQ0
PC7	E7	PC07/TMR0.B.COMP0/URT1.C.TXD/SPI2.B.MOSI/I2S2.A.MCLK/SDC0.A.RSTN
PC8	A7	PC08/TMR0.B.COMP1/URT2.C.TXD/I2C0.C.SCL/CAN2.C.TXD/I2S2.AB.TXD2/DAO.B.LN/SDC0.A.DATA4/XPL_SLV.A.ERR
PC9	C7	PC09/TMR0.B.CAPT1/URT2.C.CTS/I2C0.C.SDA/CAN2.C.RXD/I2S2.AB.TXD3/DAO.B.LP/SDC0.A.DATA5/XPL_SLV.A.RDY
PC10	B8	PC10/TMR0.B.COMP2/URT2.C.DE/URT2.C.RTS/CAN2.C.STBY/I2S2.AB.TXD0/DAO.B.RN/SDC0.A.DATA6
PC11	D8	PC11/TMR0.B.CAPT2/URT2.C.CTS/I2S2.AB.TXD1/DAO.B.RP/SDC0.A.DATA7

PC12-PC17

PC12	E9	PC12/TMR1.B.CAPT3/URT3.C.CTS/I2C1.C.SDA/SPI3.B.MISO/PDM0.CD.D0/SDC1.AC.DATA3
PC13	F10	PC13/TMR1.B.COMP3/URT3.C.DE/URT3.C.RTS/I2C1.C.SCL/SPI3.B.MOSI/CAN3.C.STBY/PDM0.C.CLK/SDC1.AC.CMD
PC14	H10	PC14/TMR0.B.CAPT3/URT3.C.RXD/SPI3.B.CS0/CAN3.C.RXD/PDM0.CD.D1/SDC1.AC.DATA2
PC15	E11	PC15/TMR0.B.COMP3/URT3.C.TXD/SPI3.B.SCLK/CAN3.C.TXD/PDM0.CD.D2/SDC1.AC.DATA1
PC16	G11	PC16/TMR3.B.COMP0/URT4.C.TXD/CAN4.C.TXD/PDM0.D.CLK/SDC1.AC.CLK
PC17	F12	PC17/TMR3.B.CAPT0/URT4.C.RXD/CAN4.C.RXD/PDM0.CD.D3/SDC1.AC.DATA0

PC18-PC29

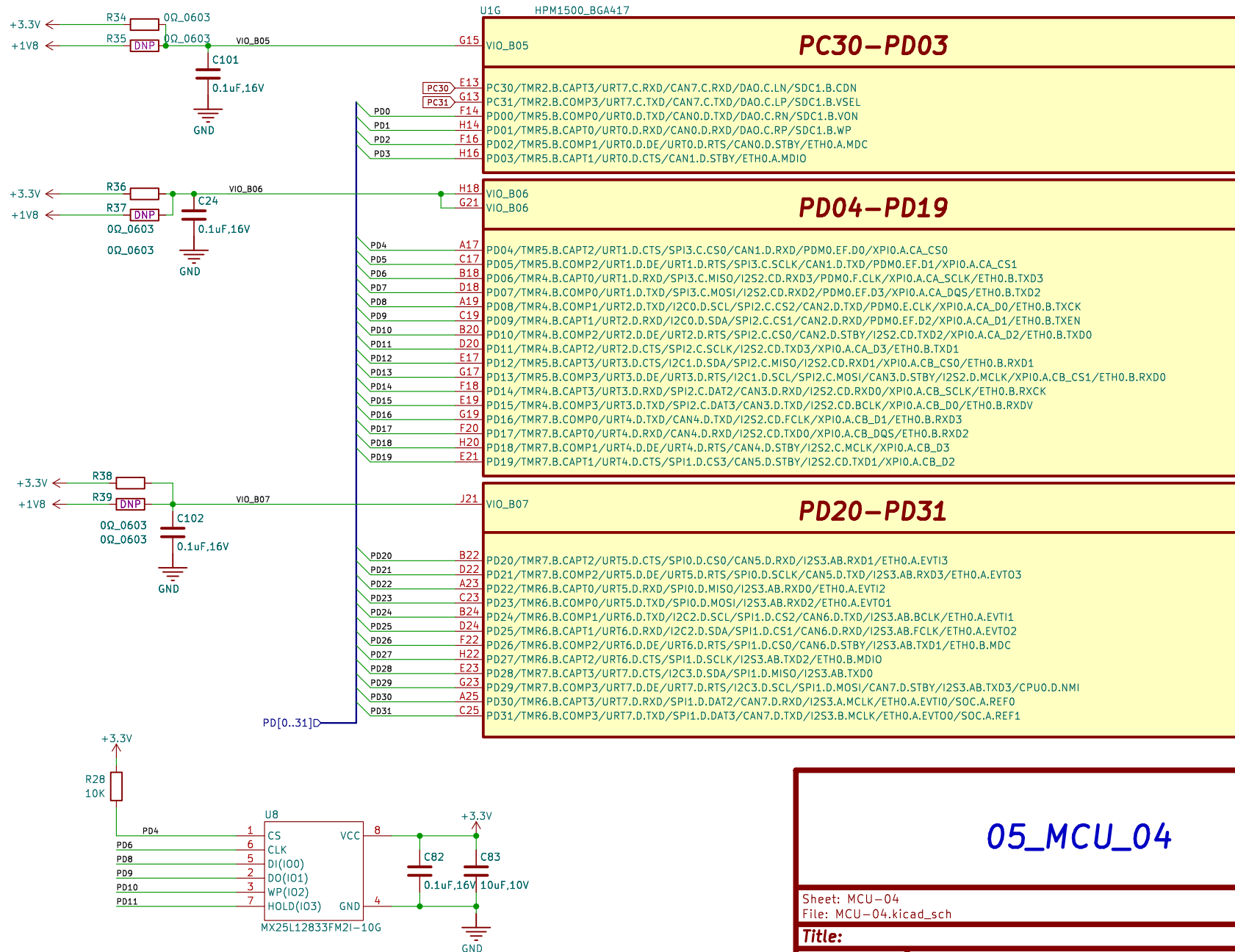
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PC19	D10	PC19/TMR3.B.CAPT1/URT4.C.CTS/SPI1.C.CS3/CAN5.C.STBY/I2S1.CD.RXD3/SDC1.B.DATA2/ADC0.A.DBG
PC20	A11	PC20/TMR3.B.CAPT2/URT5.C.CTS/SPI0.C.CS0/CAN5.C.RXD/I2S1.CD.RXD0/SDC1.B.CLK/ETH0.A.TXCK
PC21	C11	PC21/TMR3.B.COMP2/URT5.C.DE/URT5.C.RTS/SPI0.C.SCLK/CAN5.C.TXD/I2S1.CD.RXD1/SDC1.B.CMD/ETH0.A.TXEN
PC22	B12	PC22/TMR2.B.CAPT0/URT5.C.RXD/SPI0.C.MISO/I2S1.CD.BCLK/SDC1.B.DATA1/ETH0.A.TXD0
PC23	D12	PC23/TMR2.B.COMP0/URT5.C.TXD/SPI0.C.MOSI/I2S1.CD.FCLK/SDC1.B.DATA0/ETH0.A.TXD1/SYSCTL.B.CLK_OB50
PC24	A13	PC24/TMR2.B.COMP1/URT6.C.TXD/I2C2.C.SCL/SPI1.C.CS2/CAN6.C.TXD/I2S1.CD.TXD0/SDC1.C.CDN/ETH0.A.RXD2
PC25	C13	PC25/TMR2.B.CAPT1/URT6.C.RXD/I2C2.C.SDA/SPI1.C.CS1/CAN6.C.RXD/I2S1.CD.TXD1/SDC1.C.VSEL/ETH0.A.RXD3
PC26	B14	PC26/TMR2.B.COMP2/URT6.C.DE/URT6.C.RTS/SPI1.C.CS0/CAN6.C.STBY/I2S1.CD.TXD2/SDC1.C.WP/ETH0.A.RXCK
PC27	D14	PC27/TMR2.B.CAPT2/URT6.C.CTS/SPI1.C.SCLK/I2S1.CD.TXD3/SDC1.C.VON/ETH0.A.RXDV/SYSCTL.B.CLK_OB52
PC28	B16	PC28/TMR3.B.CAPT3/URT7.C.CTS/I2C3.C.SDA/SPI1.C.MISO/I2S1.C.MCLK/ETH0.A.RXD1
PC29	D16	PC29/TMR3.B.COMP3/URT7.C.DE/URT7.C.RTS/I2C3.C.SCL/SPI1.C.MOSI/CAN7.C.STBY/I2S1.D.MCLK/ETH0.A.RXD0

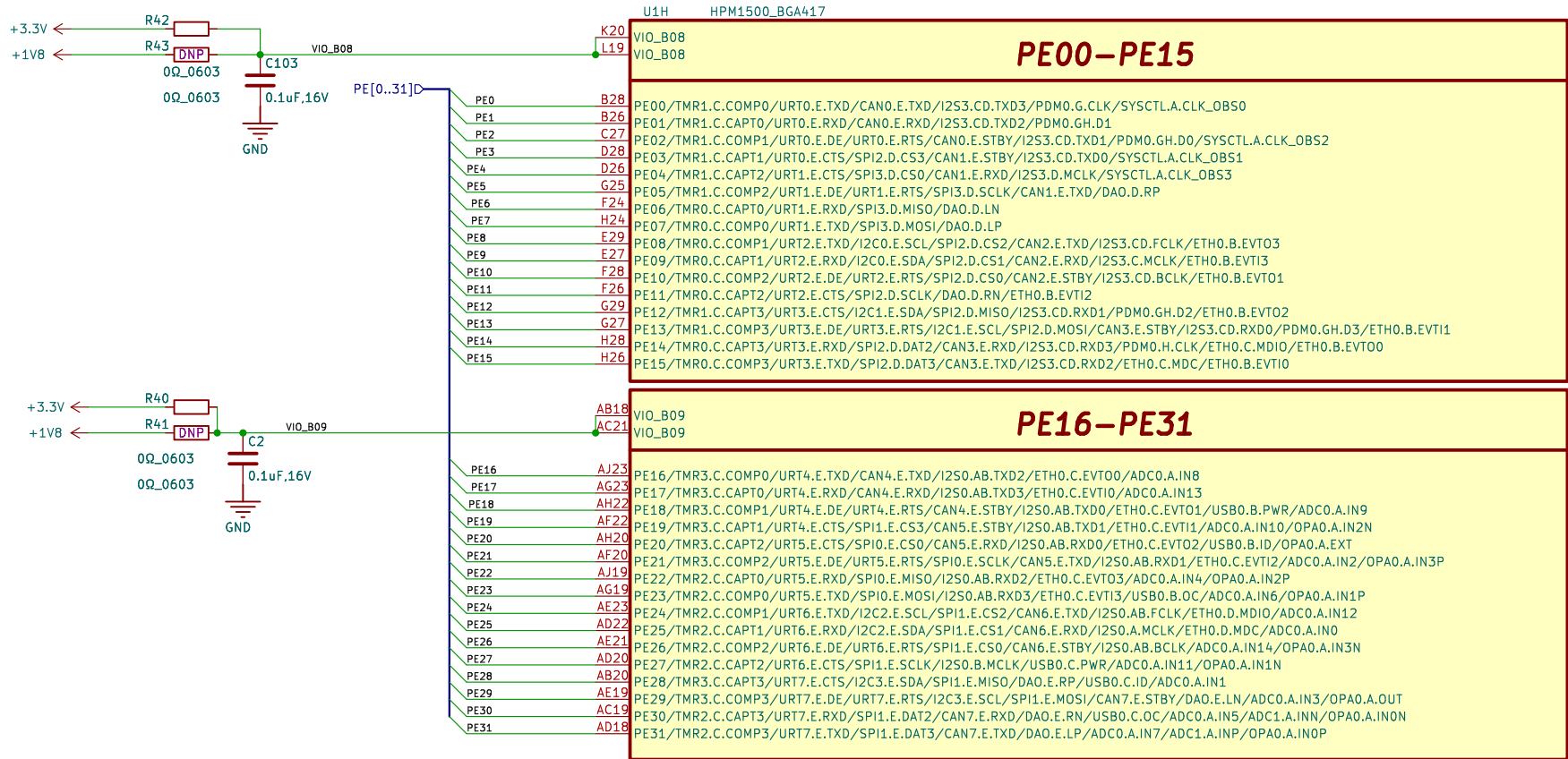
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04_MCU_03



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06_MCU_05



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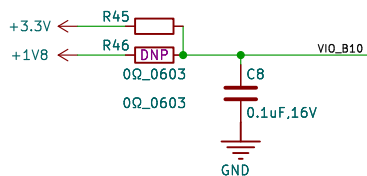
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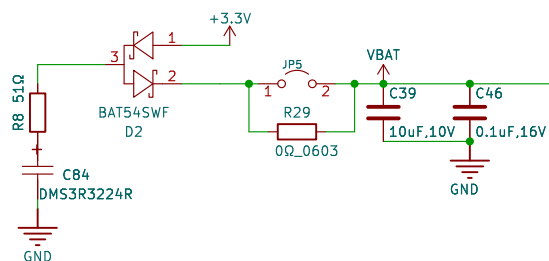
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U11	HPM1500_BGA417
AA15	VIO_B10
PF0	AH18
PF1	AF18
PF2	AJ17
PF3	AG17
PF4	AH16
PF5	AF16
PF6	AE17
PF7	AC17
PF8	AD16
PF9	AB16

PF00–PF09

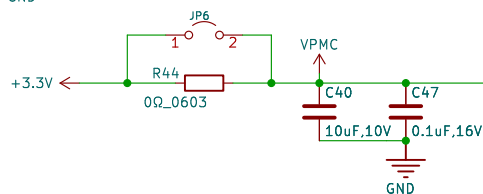
PF00	TMR5.C.COMP0/URT0.F.TXD/CAN0.F.TXD/PDM0.K.CLK/USB0.A.PWR
PF01	TMR5.C.CAPT0/URT0.F.RXD/CAN0.F.RXD/PDM0.JK.D2/CPU0.E.NMI
PF02	TMR5.C.COMP1/URT0.F.DE/URT0.F.RTS/CAN0.F.STBY/PDM0.JK.D3/ETH0.D.EVT00
PF03	TMR5.C.CAPT1/URT0.F.CTS/SPI3.E.CS3/CAN1.F.STBY/PDM0.JK.D1/ETH0.D.EVT01/USB0.A.OC
PF04	TMR5.C.CAPT2/URT1.F.CTS/SPI2.E.CS0/CAN1.F.RXD/PDM0.J.CLK/ETH0.D.EVT02/USB0.A.ID
PF05	TMR5.C.COMP2/URT1.F.DE/URT1.F.RTS/SPI2.E.SCLK/CAN1.F.TXD/PDM0.JK.D0/ETH0.D.EVT03
PF06	TMR4.C.CAPT0/URT1.F.RXD/SPI2.E.MISO/ETH0.D.EVT10
PF07	TMR4.C.COMP0/URT1.F.TXD/SPI2.E.MOSI/ETH0.D.EVT11/
PF08	TMR4.C.COMP1/URT2.F.TXD/I2C0.F.SCL/SPI3.E.CS2/CAN2.F.TXD/ETH0.E.MDIO/ETH0.D.EVT12
PF09	TMR4.C.CAPT1/URT2.F.RXD/I2C0.F.SDA/SPI3.E.CS1/CAN2.F.RXD/ETH0.E.MDC/ETH0.D.EVT13



U1J	HPM1500_BGA417
AE15	VBAT
AC15	VBAT
PZ0	AD14
PZ1	AB14
PZ2	AE13
PZ3	AC13
PZ4	AE11
PZ5	AC11
PZ6	AD10
PZ7	AB10

PZ00–PZ07

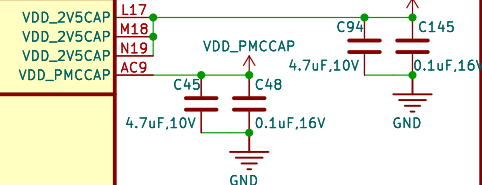
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PZ01	TMR3.D.CAPT0/URT4.G.RXD/CAN4.G.RXD
PZ02	TMR3.D.COMP1/URT4.G.DE/URT4.G.RTS/CAN4.G.STBY/DAO.F.RP
PZ03	TMR3.D.CAPT1/URT4.G.CTS/CAN5.G.STBY/DAO.F.RN
PZ04	TMR3.D.CAPT2/URT5.G.CTS/SPI0.G.CS0/CAN5.G.RXD/DAO.F.LP
PZ05	TMR3.D.COMP2/URT5.G.DE/URT5.G.RTS/SPI0.G.SCLK/CAN5.G.TXD/DAO.F.LN
PZ06	TMR2.D.CAPT0/URT5.G.RXD/SPI0.G.MISO
PZ07	TMR2.D.COMP0/URT5.G.TXD/SPI0.G.MOSI



PY0	AJ13
PY1	AG13
PY2	AH12
PY3	AF12
PY4	AJ11
PY5	AG11
PY6	AH10
PY7	AF10

PY00–PY07

PY00	TMR1.D.COMP0/URT0.G.TXD/CAN0.G.TXD
PY01	TMR1.D.CAPT0/URT0.G.RXD/CAN0.G.RXD
PY02	TMR1.D.COMP1/URT0.G.DE/URT0.G.RTS/CAN0.G.STBY/PDM0.M.CLK
PY03	TMR1.D.CAPT1/URT0.G.CTS/CAN1.G.STBY/PDM0.LM.D2
PY04	TMR1.D.CAPT2/URT1.G.CTS/SPI2.F.CS0/CAN1.G.RXD/PDM0.LM.D3
PY05	TMR1.D.COMP2/URT1.G.DE/URT1.G.RTS/SPI2.F.SCLK/CAN1.G.TXD/PDM0.LM.D1
PY06	TMR0.D.CAPT0/URT1.G.RXD/SPI2.F.MISO/PDM0.L.CLK
PY07	TMR0.D.COMP0/URT1.G.TXD/SPI2.F.MOSI/PDM0.LM.D0



PY[0..7] PY[0..7]
PZ[0..7] PZ[0..7]
PF[0..9] PF[0..9]

07_MCU_06



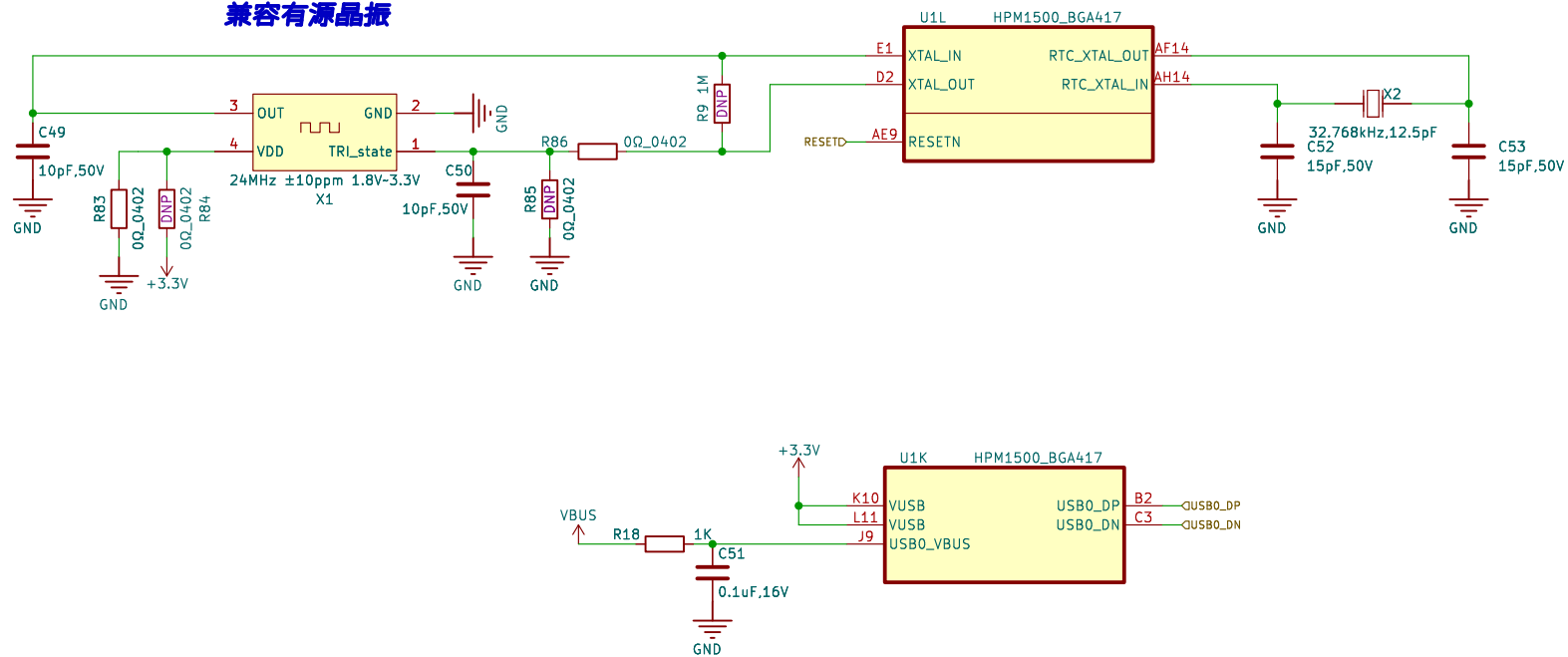
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File: MCU-06.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. kicad 7.0.2

Rev:
Id: 8/16

默认焊接无源晶振
兼容有源晶振



08_MCU_07



Sheet: MCU-07

File: MCU-07.kicad_sch

Title:

Size: A4

Date:

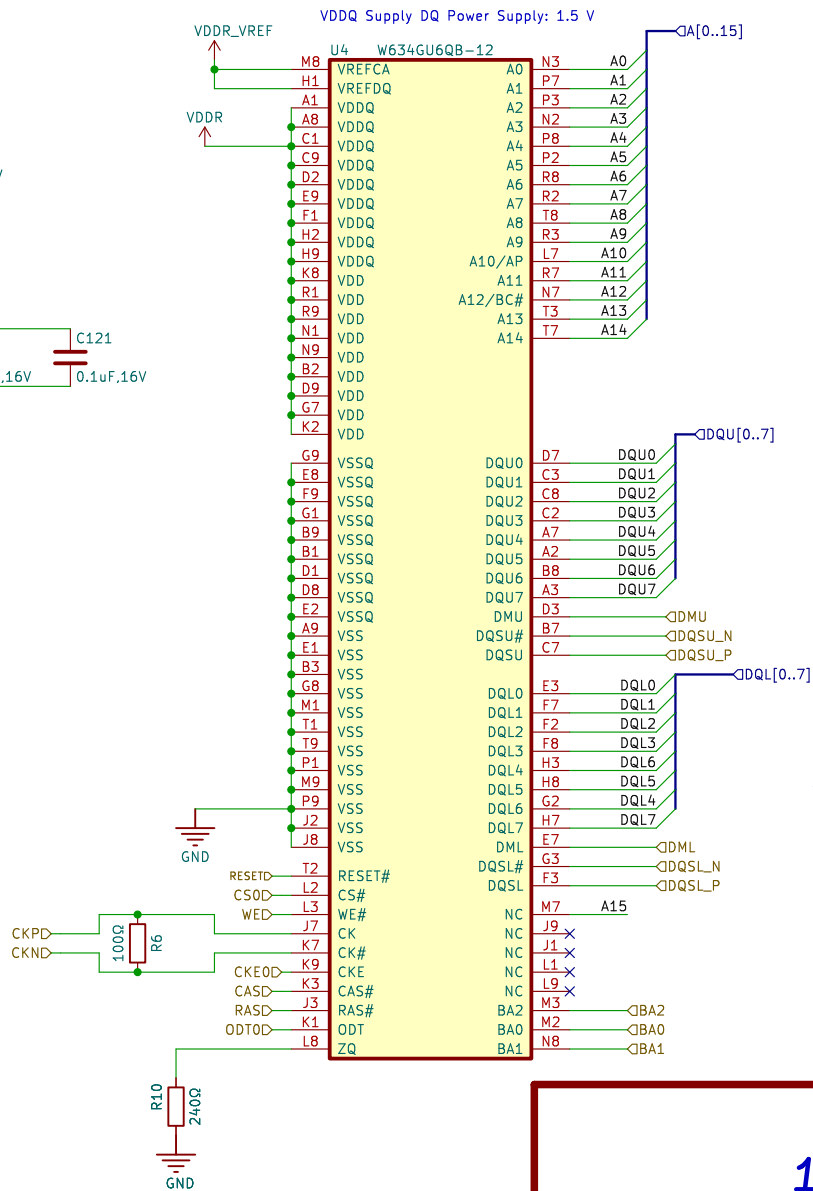
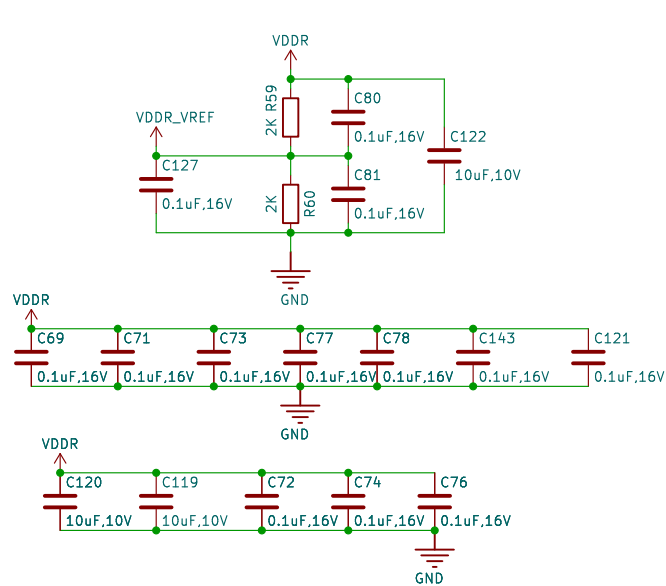
Rev:

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Id: 9/16



Id: 10/16



D6 D4 交换

11_DDR3



Sheet: DDR3

File: DDR3.kicad_sch

Title:

Size: A4

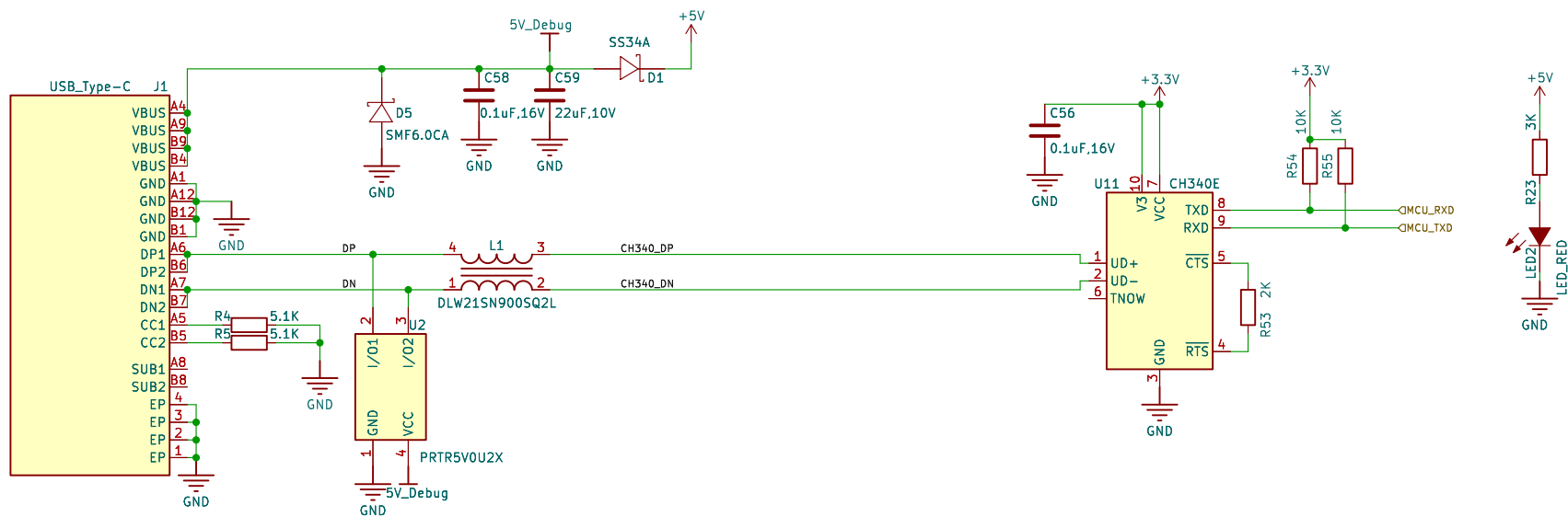
Date:

Rev:

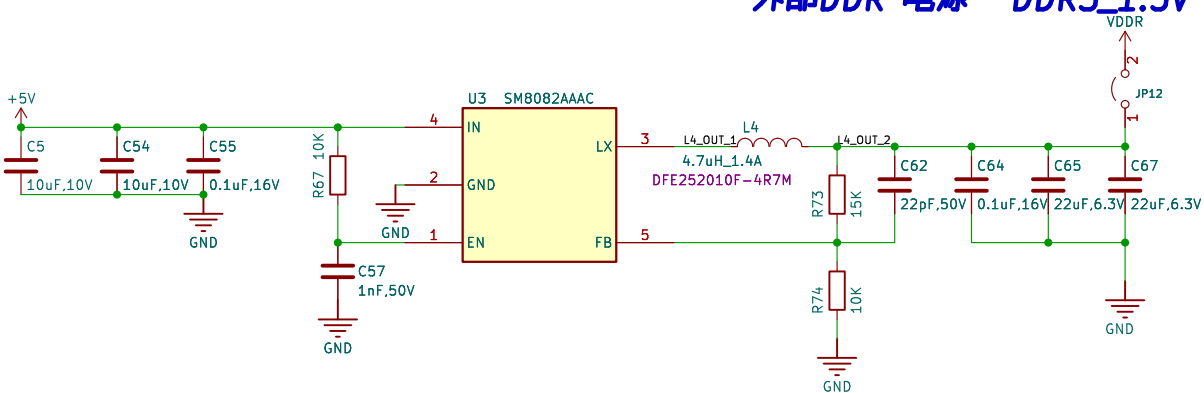
KiCad E.D.A. kicad 7.0.2

Id: 12/16

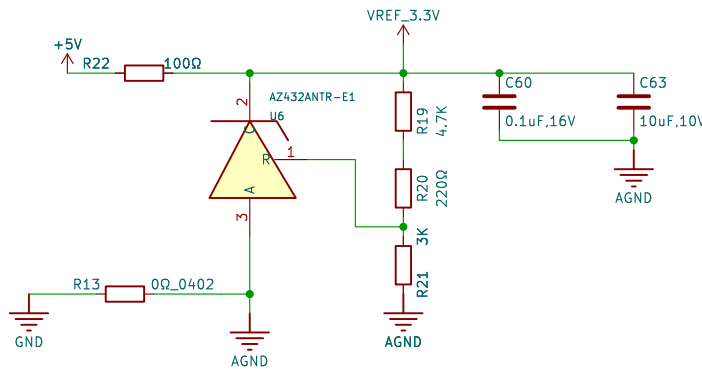
USB_UART



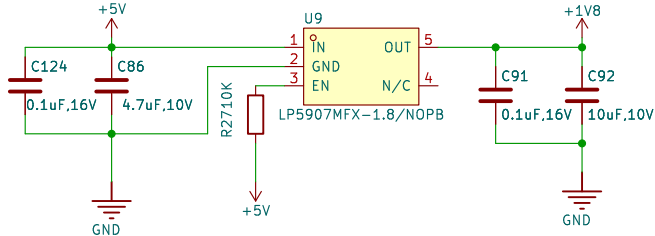
外部DDR 电源 DDR3_1.5V



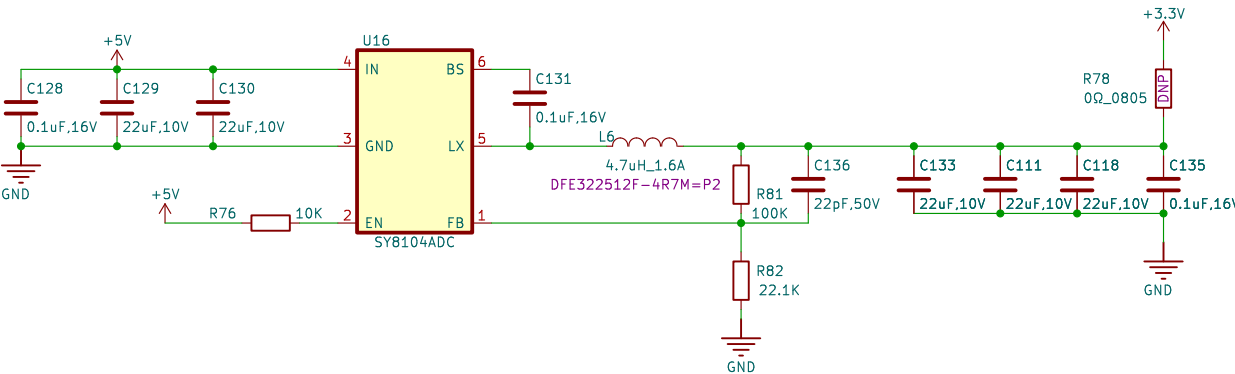
VREFH



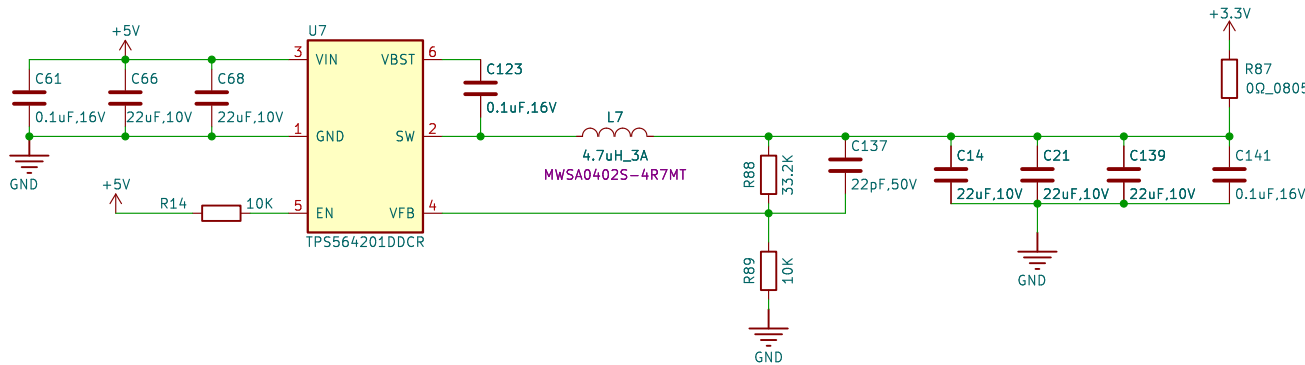
LDO-1V8



备用



SYSTEM_3V3



12_USB



Sheet: USB
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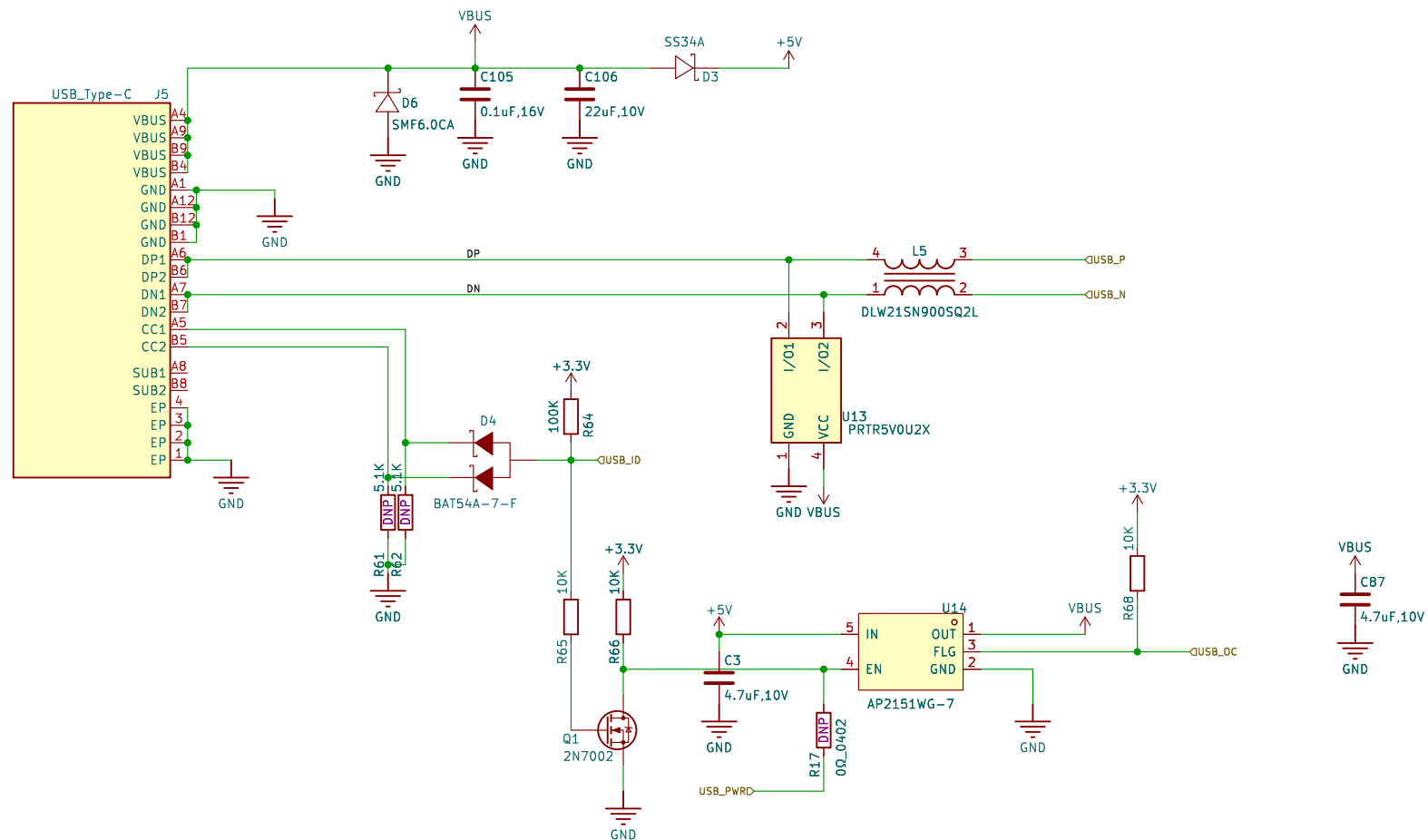
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Date:

Rev:

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Id: 13/16



13_USB_OTG



Sheet: USB_OTG
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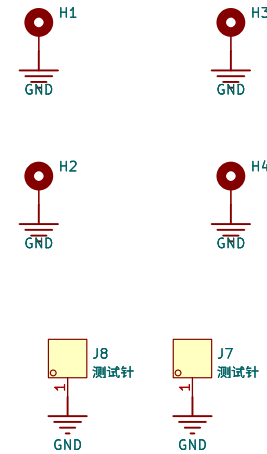
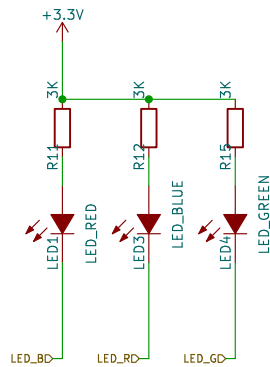
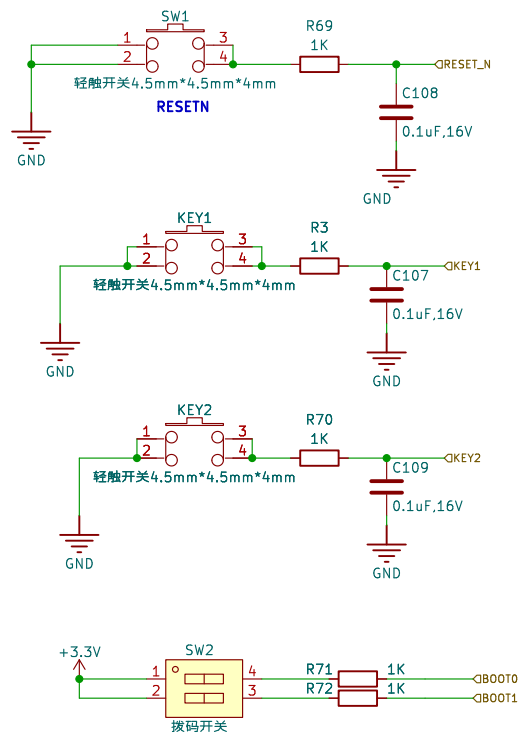
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Date:

Rev:

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Id: 14/16



14_Peripheral



Sheet: Peripheral
File: peripheral.kicad_sch

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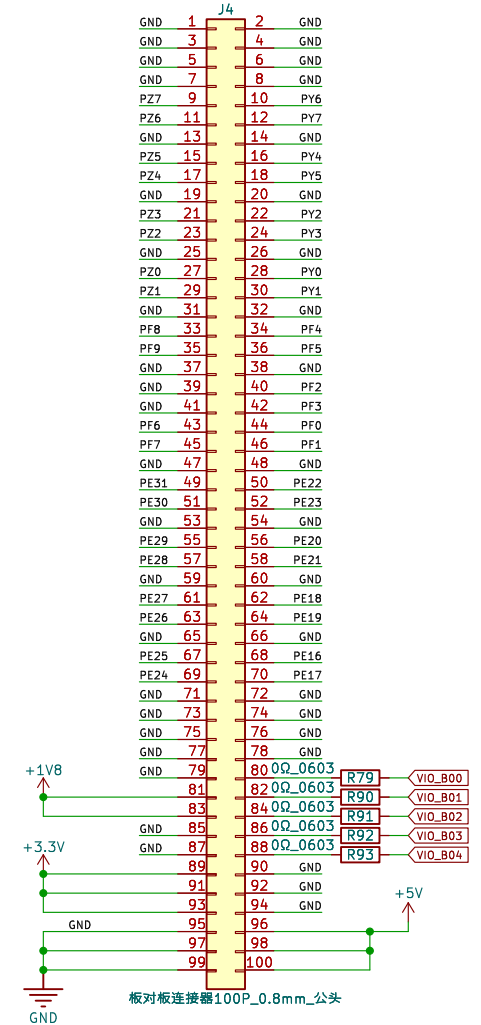
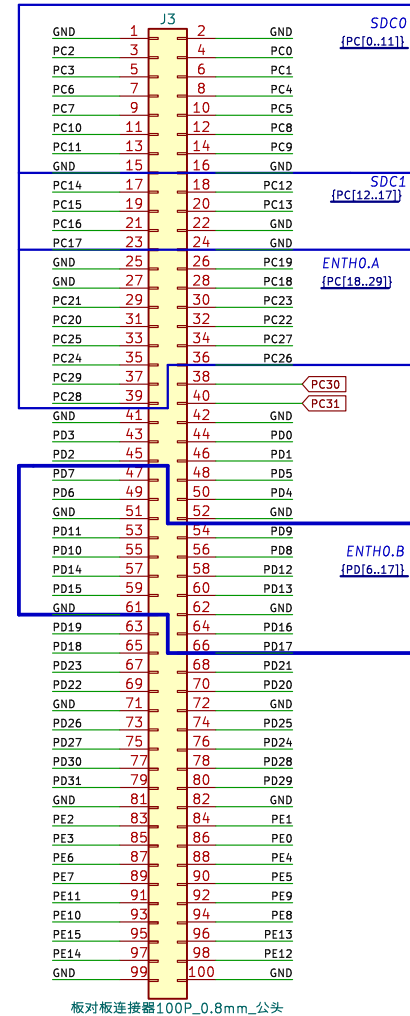
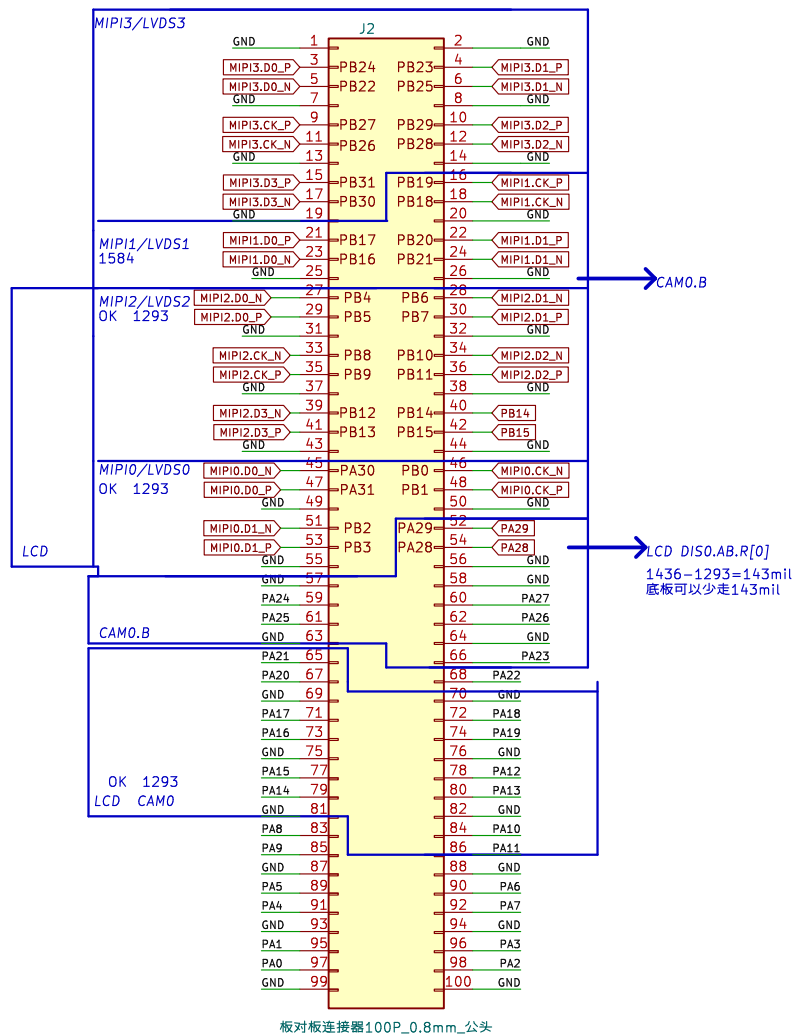
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Date:

Rev:

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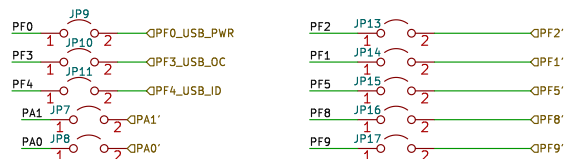


{PB30/31_N PB30/31_P PB28/29_N PB28/29_P PB26/27_N PB26/27_P PB23/25_P PB23/25_N PB22/24_N PB22/24_P}

{PB18/19_N PB18/19_P PB16/17_N PB16/17_P PB20/21_P PB20/21_N}

{PB14/15_P PB14/15_N PB10/11_P PB10/11_N PB6/7_P PB6/7_N PB12/13_P PB12/13_N PB8/9_P PB8/9_N PB4/5_P PB4/5_N}

{PB0/1_P PB0/1_N PB2/3_P PB2/3_N PA30/31_P PA30/31_N}



PA[0..27]D PA[0..27]
PC[0..29]D PC[0..29]
PD[0..31]D PD[0..31]
PE[0..31]D PE[0..31]

PY[0..7]D PY[0..7]
PZ[0..7]D PZ[0..7]
PF[0..9]D PF[0..9]

15_Connect



Sheet: Connect
File: Connect.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. kicad 7.0.2

Rev:
Id: 16/16